Please amend the claims as set forth below:

Claims 1-27 (Canceled)

28. (Currently amended) A method comprising:

provising an operating voltage to a processor configured to process wireless communication signals;

~~sensing a level of power supplied to the processor in order to determine a current mode of operation~~ determining an anticipated change in a mode of operation of the processor; and

modifying the operating voltage provided to the processor based on the anticipated change in the mode of operation of the processor.

29.-31. (Canceled)

32. (Previously presented) A method in accordance with claim 28, wherein the operating voltage is modified by reducing the operating voltage when the current mode of operation is determined to be a sleep mode.

33. (Previously presented) A method in accordance with claim 28, wherein the operating voltage is modified by increasing the operating voltage when the current mode of operation is determined to be an active mode.

34.-54. (Canceled)

55. (Currently amended) An apparatus comprising:

a power management controller to provide an operating voltage to a processor configured to process wireless communication signals, wherein the power management controller is adapted to ~~receive a signal indicating~~ determine an

anticipated mode of operation of the processor and to modify the operating voltage based on the ~~signal~~anticipated mode of operation.

56.-57. (Canceled)

58. (Currently amended) An article of manufacture comprising:

a storage medium; and

a set of instructions stored in the storage medium, which when executed by a power management controller cause the power management controller to perform operations comprising:

providing an operating voltage to a processor configured to process wireless communication signals;

~~receiving a signal indicating an anticipated mode of operation of the processor~~determining an anticipated change in the mode of operation of the processor; and

modifying the operating voltage provided to the processor based on the ~~signal~~anticipated change in the mode of operation of the processor.

59. (Previously presented) An article of manufacture in accordance with claim 58, wherein the operating voltage is modified by reducing the operating voltage in response to the signal when the anticipated mode of operation is a sleep mode.

60. (Previously presented) An article of manufacture in accordance with claim 58, wherein the operating voltage is modified by increasing the operating voltage when the anticipated mode of operation is an active mode.

61. (New) A method in accordance with claim 28, wherein the anticipated change in the mode of operation of the processor is determined by sensing a current mode of operation.

62. (New) A method in accordance with claim 28, wherein the anticipated change in the mode of operation of the processor is determined by sending a signal indicative of the current or anticipated mode.

63. (New) A method in accordance with claim 28, wherein the anticipated change in the mode of operation of the processor is determined by sending a signal indicative of the anticipated change of mode of operation.

64. (New) A method in accordance with claim 28, wherein the operating voltage provided to the processor based on the anticipated change in the mode of operation of the processor is modified based on a signal indicative of the anticipated change.